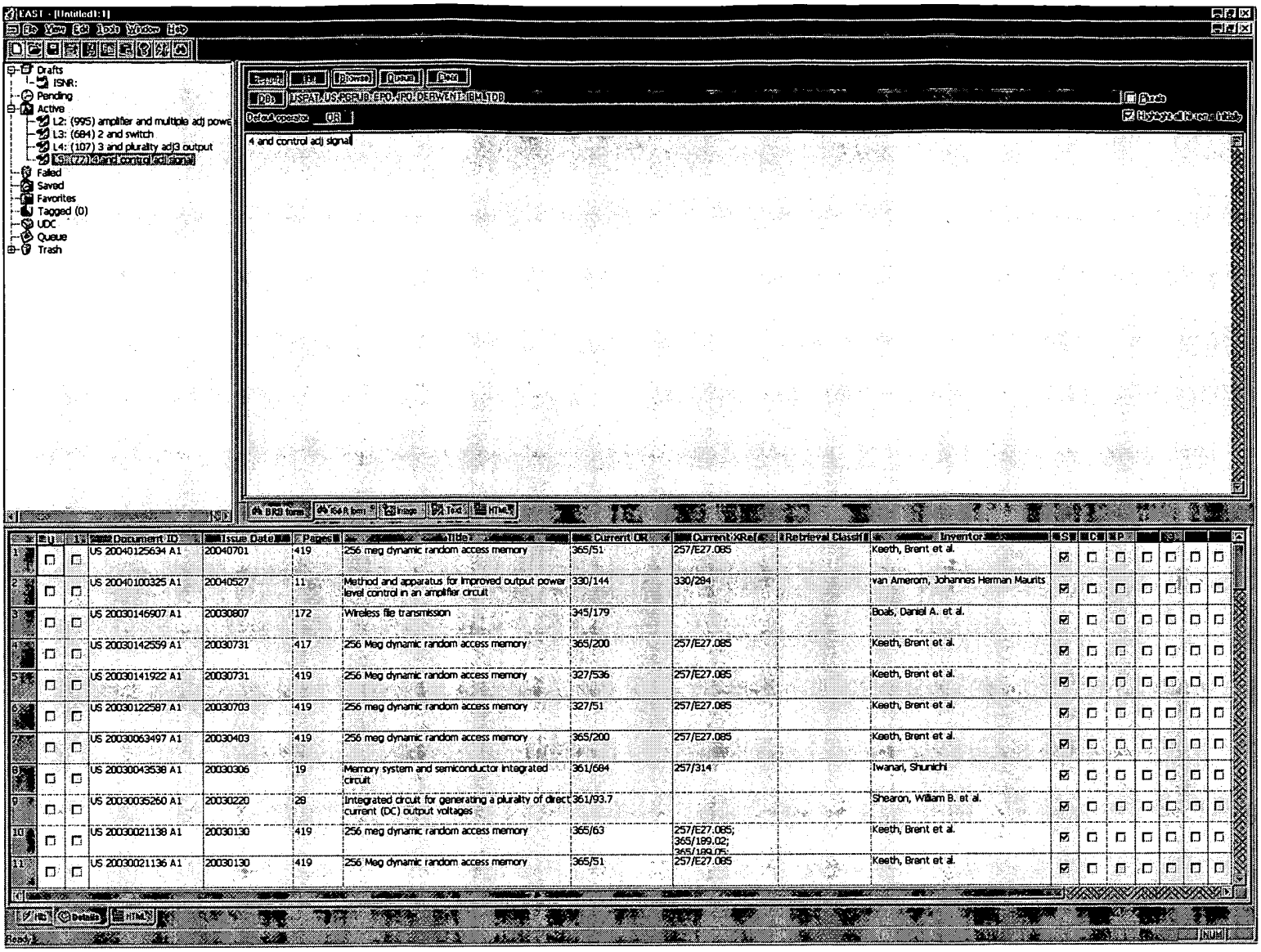


| | U | | Document ID | Issue Date | Pages | Title | Current OR | Current XRef | Retrieval Classif | Inventor | S | C | P | | | | |
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| 1 | ☐ | ☐ | US 20040125634 A1 | 20040701 | 419 | 256 meg dynamic random access memory | 365/51 | 257/E27.085 | | Keeth, Brent et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ |
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| 7 | ☐ | ☐ | US 20030063497 A1 | 20030403 | 419 | 256 meg dynamic random access memory | 365/200 | 257/E27.085 | | Keeth, Brent et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ |
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| 9 | ☐ | ☐ | US 20030035260 A1 | 20030220 | 29 | Integrated circuit for generating a plurality of direct current (DC) output voltages | 361/93.7 | | | Shearon, William B. et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ |
| 10 | ☐ | ☐ | US 20030021138 A1 | 20030130 | 419 | 256 meg dynamic random access memory | 365/63 | 257/E27.085; 365/189.02; 365/189.05 | | Keeth, Brent et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ |
| 11 | ☐ | ☐ | US 20030021136 A1 | 20030130 | 419 | 256 Meg dynamic random access memory | 365/51 | 257/E27.085 | | Keeth, Brent et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ |